	Application No.	Applicant(s)		
	10/807,973	MICHALSKI, CHR	MICHALSKI, CHRISTOPHER	
	Examiner	Art Unit		
	My-Trang N. Ton	2816		
— The MAILING DATE of this communication appea. All claims being allowable, PROSECUTION ON THE MERITS 15 (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIC of the Office or upon petition by the applicant. See 37 CFR 1.313: 1. ☑ This communication is responsive to Amdt. filed 8/15/05.	OR REMAINS) CLOSED in this or other appropriate communica SHTS. This application is subjection.	s application. If not inclu	ided	
2. 🔀 The allowed claim(s) is/are <u>1-4, 6-32</u> .				
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some* c) None of the: 1. Cartified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No				
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/S8/08), Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summa Paper No./Mail D 7. ☐ Examiner's Amen	Date .	owance	

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Reasons for allowance

The following is an examiner's statement of reasons for allowance:

None of the prior art disclosed or suggested to show the particular structure and/or the particular operation recited in these claims namely: "the gates of the first and second NMOS devices being coupled together through a bias control, the bias control providing a different bias to the gates of the first and second NMOS devices" as recited in claim 1; "the gate of the second NMOS device being coupled to the source of the first NMOS device" as recited in claim 8; the combination of "a first NMOS device", "a second NMOS device" and "a current source" in claim 14; "the gate of the second NMOS device being coupled to the source of the first NMOS device" and "a current source" as recited in claim 21; the combination of "the first – fourth NMOS devices" as recited in claim 27; "the bias control is coupled to the gate of the second NMOS device through a resistor, and wherein the gates of the first and second NMOS devices are AC coupled together" as recited in claims 31 and 32.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

My-Trang N. Ton Primary Examiner Art Unit 2816

October 20, 2005